What is claimed is:

1. A driver which has a storage device for storing display data and drives a display section based on the display data, wherein the storage device comprises:

a plurality of memory cells;

a plurality of first word lines, one of which is selected to write the display data to part of the memory cells;

a plurality of second word lines paired with the respective first word lines, one of the second word lines being selected to read out the display data in part of the memory cells;

a plurality of first bit lines to be used for writing the display data to part of the memory cells associated with a selected one of the first word lines;

a plurality of second bit lines paired with the respective first bit lines and to be used for reading out the display data in part of the memory cells associated with a selected one of the second word lines;

a pre-charging circuit which pre-charges the second bit lines;

a plurality of first switching elements provided on pre-charging paths between the pre-charging circuit and the second bit lines; and

a plurality of first control lines which controls on/off state of the first switching elements according to timing of reading out the display data stored in the respective memory cells.

2. The driver as defined in claim 1, wherein the storage device further comprises:

a plurality of second switching elements having one ends that are connected to the second bit lines and the other ends that are grounded; and

a plurality of second control lines which controls on/off state of the second switching elements according to the same timing as the first switching elements are turned on or off by means of the first control lines.

3. The driver as defined in claim 2,

wherein the display data is N-bit information (N is a natural number) and the plurality of first control lines are N lines; and

wherein each of the N first control lines is used for on/off-controlling part of the first switching elements that are connected to part of the second bit lines, respectively, the part of the second bit lines being used for reading pieces of one-bit information that corresponds to a particular order bit among N-bit information stored in the memory cells.

4. The driver as defined in claim 3,

wherein the plurality of second control lines are N lines that are paired with the respective N first control lines; and

wherein each of the N second control lines is used for on/off-controlling part of the second switching elements that

are connected to part of the second bit lines, respectively, the part of the second bit lines being used for reading pieces of one-bit information that corresponds to a particular order bit among N-bit information stored in the memory cells.

5. An electro-optical device comprising the driver as defined in claim 1.

6. An electronic apparatus comprising the electro-optical device as defined in claim 5.